Docket No.: MAS-FIN-153

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant

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JOHANN WINDERL

Filed

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CONCURRENTLY HEREWITH

Title

ELECTRONIC COMPONENT WITH STACKED SEMICONDUCTOR

CHIPS

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks, Washington, D.C. 20231

Sir:

In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent 5,222,014 (Lin), dated June 22, 1993;

U.S. Patent 5,608,265 (Kitano et al.), dated March 4, 1997;

U.S. Patent 5,861,666 (Bellaar), dated January 19, 1999;

U.S. Patent 5,682,062 (Gaul), October 28, 1997;

U.S. Patent US 6,180,881 B1 (Isaak), dated January 30, 2001;

Patent Abstracts of Japan 04 030 561 A (Ryosuke et al.), dated February 3, 1992;

Patent Abstracts of Japan 11 354 683 A (Seiko), dated December 24, 1999.

Respectfully submitted,

For Applicant

LAURENCE A. GREENBERG REG. NO. 29,308

Date: March 4, 2002

Lerner and Greenberg, P.A. Post Office Box 2480

Hollywood, FL 33022-2480

Tel: (954) 925-1100 Fax: (954) 925-1101

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE				Attorney Docket No.: MAS-FIN-153 Appl. No.				
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (37 CFR 1.98(b))				Applicant JOHANN WINDERL				
				Filing Date March 4, 2002 Group Art Unit				
EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB	FILI	
	Α	5,222,014	6/22/93	Lin	33.133	02,100		16
	В	5,608,265	3/4/97	Kitano et al.		1	†	
	С	5,861,666	1/19/99	Bellaar	_		1	
	D	5,682,062	10/28/97	Gaul			 	
	E	6,180,881 B1	1/30/01	Isaak		 		
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		FOREI	GN PATEN	NT DOCUMENT				
		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRAN	
	J	04 030 561 A	2/3/92	Japan	OLAGG	CLASS	YES	NO
	K	11 354 683 A	12/24/99	Japan			-+	
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